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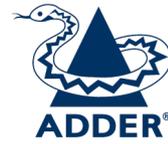
www.adder.com

Letter of Volatility
XD600 Series (XD642, XD641, XD614 & XD612)

To whom it may concern,

The XD600 Series of extenders comprises of a System-on-Chip (SoC), a processor subsystem, a variety of I/O devices including high speed serial transceivers (SERDES), external NAND flash memories for the SoC, USB, PHY & Audio circuitry and a power management subsystem.. Interconnects between these circuits are not accessible from outside the unit. Units all have the memory elements detailed below unless otherwise noted.

Memory Type	Function /Purpose	User Accessible ?	Size	Volatile / Non Volatile	Process to clear
FLASH	Serial NAND Flash Program code; SoC configuration bitstreams; Unit identification (serial number); Configuration data	No (1)	1GB	Non-volatile	Perform Factory reset to erase user data and load default values
FLASH (XD642 Only)	Serial NOR Flash Configuration bitstream for USB PHY	No(1)	4 Mbit	Non-volatile	None (No user data)
FLASH	Serial NOR Flash Configuration bitstream for Copper PHY	No(1)	4 Mbit	Non-volatile	None (No user data)
FLASH	Processor Subsystem Main program code	No (1)	512KB	Non-volatile	None (No user data)
SRAM	Processor Subsystem Temporary storage of working data	No	96KB	Volatile	Power off
FRAM	Power management subsystem Ferroelectric RAM Program code	No(2)	2 KB	Non-volatile	None (No user data)
SRAM	Power management subsystem Temporary storage of working data	No	1024 Bytes	Volatile	Power off
SRAM (XD642 Only)	USB ULPI Transceiver Defined configuration registers providing temporary storage	No	512 Bits	Volatile	Power off
SRAM	Quad clock generator	No	2800 Bits	Volatile	Power off
SRAM	Copper PHY Defined registers providing temporary storage.	No	311298 Bits	Volatile	Power off
SRAM	USB Hub Defined configuration registers providing temporary storage	No	2KB	Volatile	Power off
SRAM	SoC on-chip SRAM Provides temporary storage of program code and working data.	No	Unknown	Volatile	Power off
SRAM	LED Driver Defined registers providing temporary storage	No	224 Bits	Volatile	Power off
SRAM	Audio Codec Defined registers providing temporary storage	No	558 Bits	Volatile	Power off



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- (1) *Content modification by user limited to downloading of digitally signed Adder firmware upgrade images and entering of configuration data using the provided on-screen display (OSD)*
- (2) *Content can only be altered via a return to factory and performing a hardware upgrade using a custom factory reconfiguration utility.*

End of document.

Created By: Darrin Steel

Date: 4th March 2022

Authorized By: Mark Kennedy

Position: Director of Governance & Compliance

Signed: